AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions and listings of claims in the application:

Listing of Claims

1. – 18. (Canceled)

19. (Currently Amended) A balanced crystal oscillator circuit having a piezoelectric element, a first oscillator sub-circuit incorporating a first transistor and a second oscillator sub-circuit incorporating a second transistor, wherein the transistors are of the Bipolar Junction Transistor (BJT) type and each have different types of transistor terminals and wherein the oscillator sub-circuits are configured with a first, second and third interconnection, the circuit further comprising:

each interconnection being made at a pair of like type of transistor terminals of the first and second transistors;

the first interconnection being between a pair of like type of transistor terminals and a connection to a ground reference;

the second interconnection being between a pair of like type of transistor terminals with a first resonator element therein-between; and

the third interconnection being between a pair of like type of transistor terminals with a second resonator element therein-between, wherein at least one of the transistors is provided with bias current and said first and second oscillator circuits being arranged to interact by means of said first and second resonator elements to form a balanced oscillator signal; and

wherein the first, second, and third of said interconnections consist of a pair of collector-type terminals, base-type terminals, and emitter-type terminals, respectively; thereby configuring the balanced oscillator circuit with a dual common-collector transistor coupling.

20. (Previously Presented) The balanced crystal oscillator circuit according to claim 19, wherein the balanced oscillator signal is provided at a first circuit junction and at a second circuit junction coupled to a first and second terminal, respectively, from one of the pairs of like type of transistor terminals interconnected by either the first or second resonator element.

21. – 22. (Canceled)

23. (Currently Amended) The balanced crystal oscillator circuit according to claim [[22]] 19, wherein the first resonator element is a piezoelectric element and the second resonator element is a capacitor.

24. - 26. (Canceled)

27. (Currently Amended) The balanced crystal oscillator circuit according to claim 19, wherein at least one of the transistors is provided with bias current by means of a resistor coupled between the emitter of a transistor and a supply voltage.

28. (Canceled)

29. (Currently Amended) The balanced crystal oscillator circuit according to claim [[28]] 19, wherein the transistors are operated in class C.

30. (Currently Amended) ~~The balanced crystal oscillator circuit according to claim 19,~~ A balanced crystal oscillator circuit having a piezoelectric element, a first oscillator sub-circuit incorporating a first transistor and a second oscillator sub-circuit incorporating a second transistor, wherein the transistors are of the Metal Oxide Semiconductor (MOS) type and each have different types of transistor terminals and wherein the oscillator sub-circuits are configured with a first, second and third interconnection, the circuit further comprising:

each interconnection being made at a pair of like type of transistor terminals of the first and second transistors;

the first interconnection being between a pair of like type of transistor terminals and a connection to a ground reference;

the second interconnection being between a pair of like type of transistor terminals with a first resonator element therein-between; and

the third interconnection being between a pair of like type of transistor terminals with a second resonator element therein-between;

wherein at least one of the transistors is provided with bias current and said first and second oscillator circuits being arranged to interact by means of said first and second resonator elements to form a balanced oscillator signal, and wherein the first, second, and third of said interconnections consist of a pair of drain-type terminals, gate-type terminals, and source-type terminals, respectively; thereby configuring the balanced oscillator circuit with a dual common-drain transistor coupling.

31. (Canceled)

32. (Currently Amended) The balanced crystal oscillator circuit according to claim [[31]] 30, wherein the first resonator element is a piezoelectric element and the second resonator is a capacitor.

33. – 34. (Canceled)

35. (Currently Amended) The balanced crystal oscillator circuit according to claim 19 wherein the oscillator circuit is configured with ~~an RC-circuit~~ a circuit forming a loop-gain pole in the frequency range above a primary oscillating frequency of the oscillating output signal.

36. (Previously Presented) The balanced crystal oscillator circuit according to claim 19, wherein the non-resonator elements of the circuit are implemented on an

integrated circuit, said integrated circuit having terminals for electric interconnection with the resonator elements.

37. (Previously Presented) The balanced crystal oscillator circuit according to claim 36, wherein one of the resonator elements is a piezoelectric element, said integrated circuit having terminals for electric interconnection with the piezoelectric element.

38. (Previously Presented) The balanced crystal oscillator circuit according to claim 19, implemented in a mobile telephone.

39. (New) The balanced crystal oscillator circuit according to claim 35 wherein the circuit forming a loop-gain pole in the frequency range above a primary oscillating frequency of the oscillating output signal is a resistive-capacitive (RC) circuit.

40. (New) The balanced crystal oscillator circuit according to claim 30 wherein the oscillator circuit is configured with a circuit forming a loop-gain pole in the frequency range above a primary oscillating frequency of the oscillating output signal.

41. (New) The balanced crystal oscillator circuit according to claim 40 wherein the circuit forming a loop-gain pole in the frequency range above a primary oscillating frequency of the oscillating output signal is a resistive-capacitive (RC) circuit.

42. (New) The balanced crystal oscillator circuit according to claim 30, wherein the non-resonator elements of the circuit are implemented on an integrated circuit, said integrated circuit having terminals for electric interconnection with the resonator elements.

43. (New) The balanced crystal oscillator circuit according to claim 42, wherein one of the resonator elements is a piezoelectric element, said integrated circuit having terminals for electric interconnection with the piezoelectric element.

44. (New) The balanced crystal oscillator circuit according to claim 30, implemented in a mobile telephone.